

Atty.Dkt.No.: 5957-82403

Examiner: Richard A. Booth

Group/Art Unit: 2812

Title: Semiconductor Device Including Transistor with Composite Gate Structure and Transistor with Single Gate Structure, and Method for Manufacturing the Same

CERTIFICATE OF E-FILING TRANSMISSION

I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below:

Dean M. Munyon

Printed Name

/Dean M. Munyon/

Signature _____

March 28, 2007

Date _____

RESPONSE TO OFFICE ACTION MAILED JANUARY 25, 2007

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to an Office Action of January 25, 2007, to further highlight why the application is in condition for allowance.

Please amend the application as listed below.